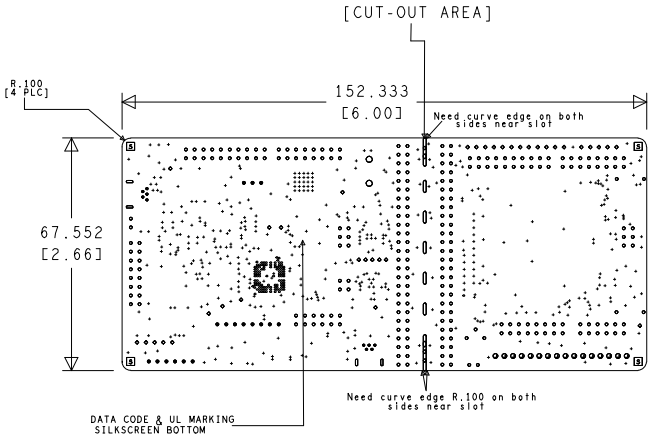
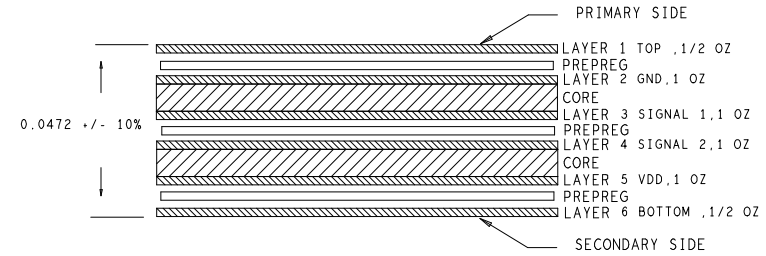
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
01	ALPHA RELEASE	07/25/15	VRNK
02	BETA RELEASE	08/13/15	RKPM
03	BETA RELEASE	08/19/15	RKPM
04	BETA RELEASE	08/26/15	RKPM
05	ES10 BUILD	09/03/15	RKPM
06	ES10+4 BUILD	09/21/15	RKPM

DRILL CHART: PSoC 4L Rev06				
ALL UNITS ARE IN MILS				
FIGURE	SIZE	TOLERANCE	PLATED	QTY
·	6.0	+3.0/-3.0	PLATED	109
·	8.0	+3.0/-3.0	PLATED	507
·	28.0	+3.0/-3.0	PLATED	10
◦	40.0	+3.0/-3.0	PLATED	259
·	40.157	+2.0/-3.0	PLATED	17
◦	43.0	+3.0/-3.0	PLATED	36
◎	47.0	+3.0/-3.0	PLATED	16
○	66.9	+2.0/-2.0	NON-PLATED	2
▭	125.0	+2.0/-2.0	NON-PLATED	4
▭	74.799x27.598	+3.0/-3.0	PLATED	2
▯	74.799x27.598	+3.0/-3.0	PLATED	2
▯	135.0x30.0	+2.0/-2.0	NON-PLATED	12



NOTES:(UNLESS OTHERWISE SPECIFIED)

1. RoHS COMPLIANT CERTIFICATION OR MATERIAL DECLARATION REQUIRED.
2. MATERIAL:
   A. IS410 OR EQUIVALENT. MATERIAL MUST CONFORM TO UL94V-0.
   B. REFER TO AS MENTIONED IN CROSS SECTION AREA IN THIS DOCUMENT.
   C. OVERALL METAL TO METAL THICKNESS AS SPECIFIED IN THE CROSS SECTION DRAWING IN THE DOCUMENT.
3. DRILLING:
   A. DIAMETERS IN DRILL TABLE ARE FINISHED HOLE SIZES +/-.003 tol.
      UNLESS OTHERWISE SPECIFIED IN DRILL TABLE.
   B. TEARDROP ALLOWED ON ENTRY OF VIA ON EVERY TRACE LAYER.
4. PLATING:
      COPPER PLATING IN THRU-HOLES .001 min.
5. MARKING:
   A. SILKSCREEN IN WHITE NON-CONDUCTIVE EPOXY INK ON PRIMARY SIDE OF THE
      BOARD OR BOTH SIDES IF APPLICABLE.
   B. FABRICATOR TO PLACE DATE CODE, LOGO AND RoHS COMPILANT SYMBOL IN SILKSCREEN ON SECONDARY SIDE.
6. FINAL FABRICATION:
   A. SOLDRMASK PRIMARY AND SECONDARY SIDE OF BOARD USING LIQUID PHOTOIMAGABLE
      MASK MATERIAL OVER BARE COPPER. PER IPC-SM-840. MASK ARTWORKS PROVIDED
      ARE 1:1. SOLDERMASK COLOR **RED**
7. FINISH:
   A. SHALL BE IMMERSION GOLD(ENIG).
   B. NICKEL THICKNESS: 0.00010-0.00020in
   C. GOLD THICKNESS: 0.0000030-0.000010in
8. BOARDS SHALL BE PURCHASED FROM UL RECOGNIZED VENDORS ONLY.
   FLAMABILITY RATING (94-V0).
9. MANUFACTURE BOARD TO BE IN ACCORDANCE WITH PERFORMANCE STANDARD
   IPC-6011/6012 CLASS 2 BOARD TO BE INSPECTED PER IPC-600-A CLASS 2.
10. MAXIMUM WRAP OR TWIST SHALL NOT EXCEED .007 in/in.
11. TESTING:
   A. FABRICATOR TO ADD TEST STRUCTURES OR CUPONS AS NEEDED.
   B. T-LINE IMPEDENCE TO BE TESTED OR GUARANTEE WITHIN 10% IF SPECIFIED IN
      THE CROSS SECTION. TOLERANCE TO BE +/-10% UNLESS OTHERWISE SPECIFIED.



90E IMPEDANCE CONTROL ON 9MIL TRACE WIDTH AND 16MIL SPACING FOR USB CONNECTOR(REFDES U10,U11)

UNLESS OTHERWISE SPECIFIED	SIGNATURES		DATE
DIMENSIONS ARE IN INCHES	DRAWN	PRAO	09/03/15
TOLERANCES ON; ANGLES +/- 2°	CHECKED	RKPM	09/03/15
2 PL DECIMALS +/- .010	ENGRG	RKPM	09/03/15
3 PL DECIMALS +/- .005	ISSUED		
THIRD ANGLE PROJECTIONS			

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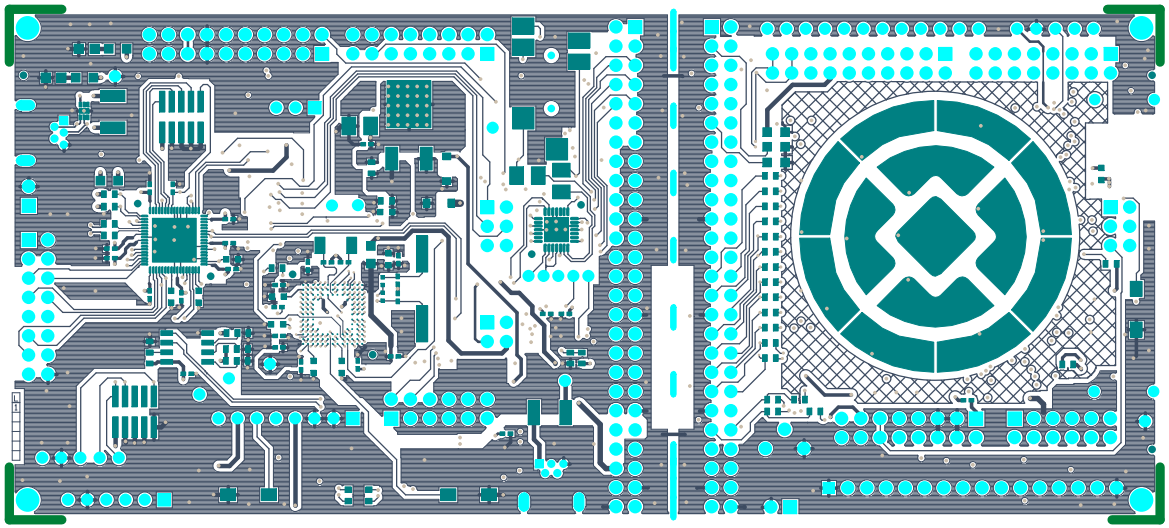
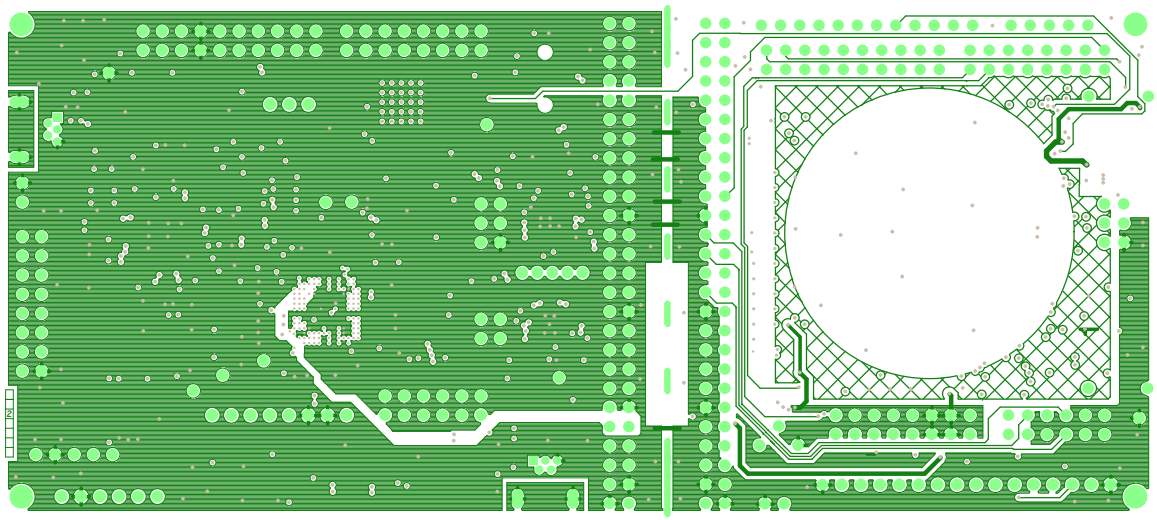
CYPRESS SEMICONDUCTOR
198 CHAMPION COURT
SAN JOSE, CA 95134
(408) 943-2600

FABRICATION DRAWING
CY8CKIT-046 PSoC 4 L-Series Pioneer Kit

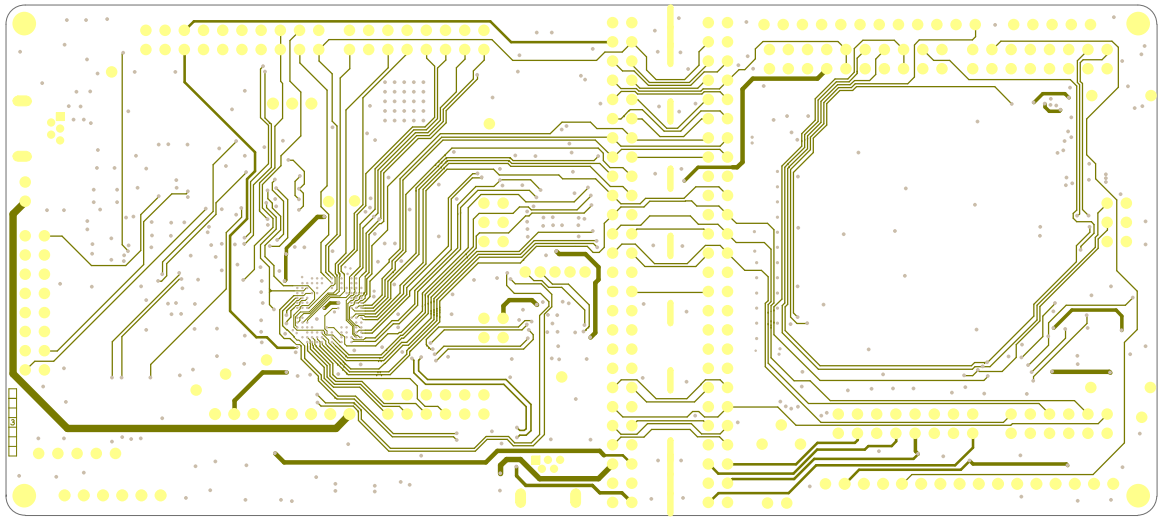
SIZE	FSCM NO	DWG NO	REV
C		610-60265-01	06
SCALE: 1/1		SHEET: 1 OF 1	



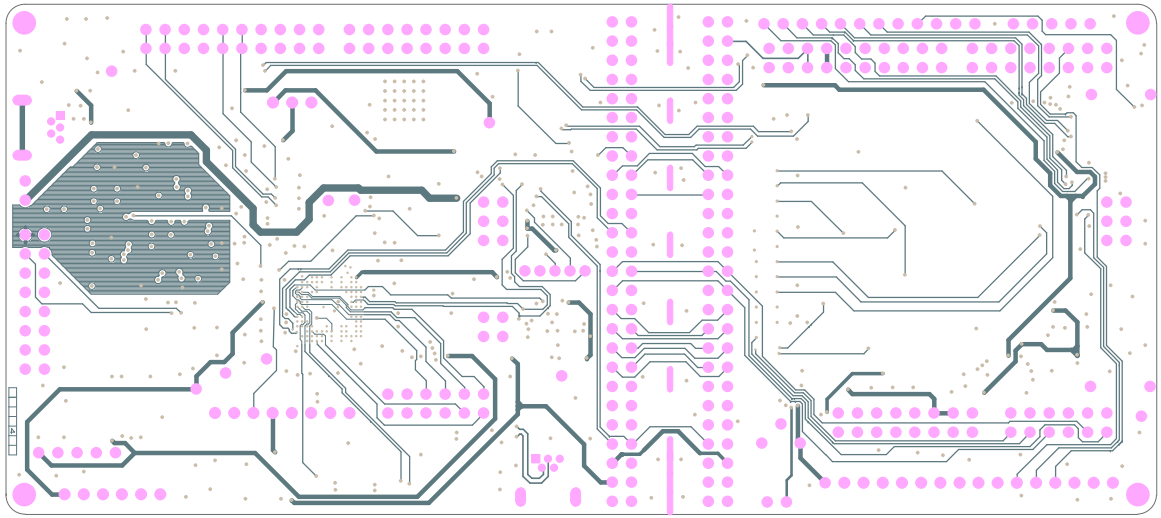
600-60276-01 REV06 PRIMARY SIDE



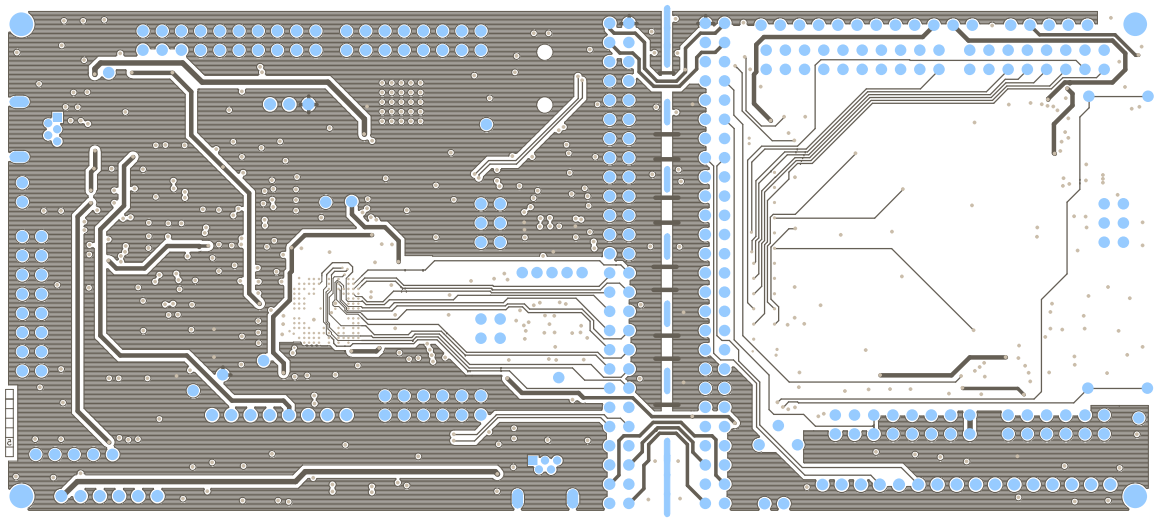
600-60276-01 REV06 GND LAYER



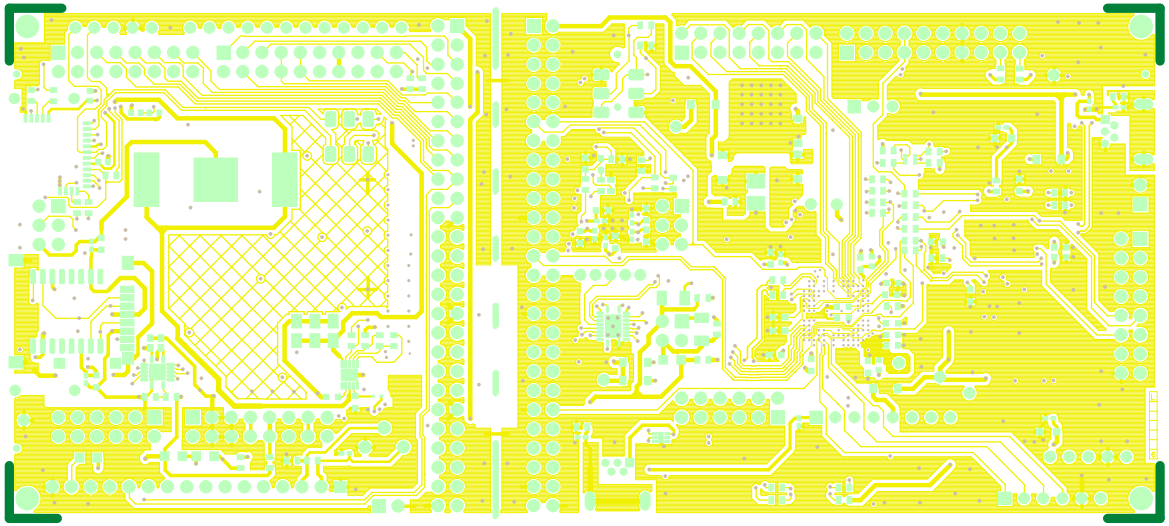
600-60276-01 REV06 SIGNAL1



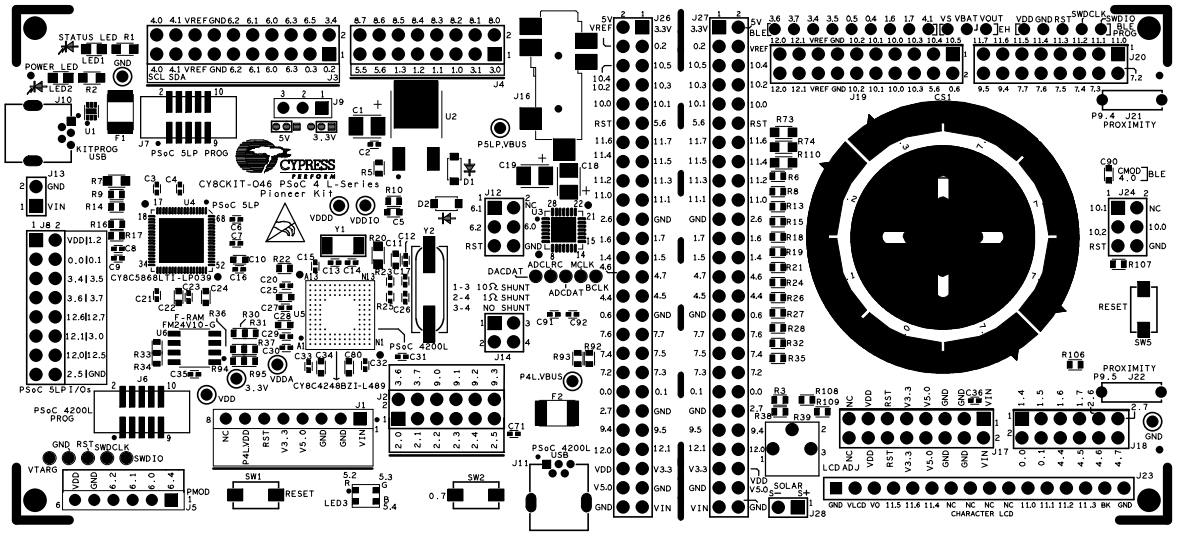
600-60276-01 REV06 SIGNAL2



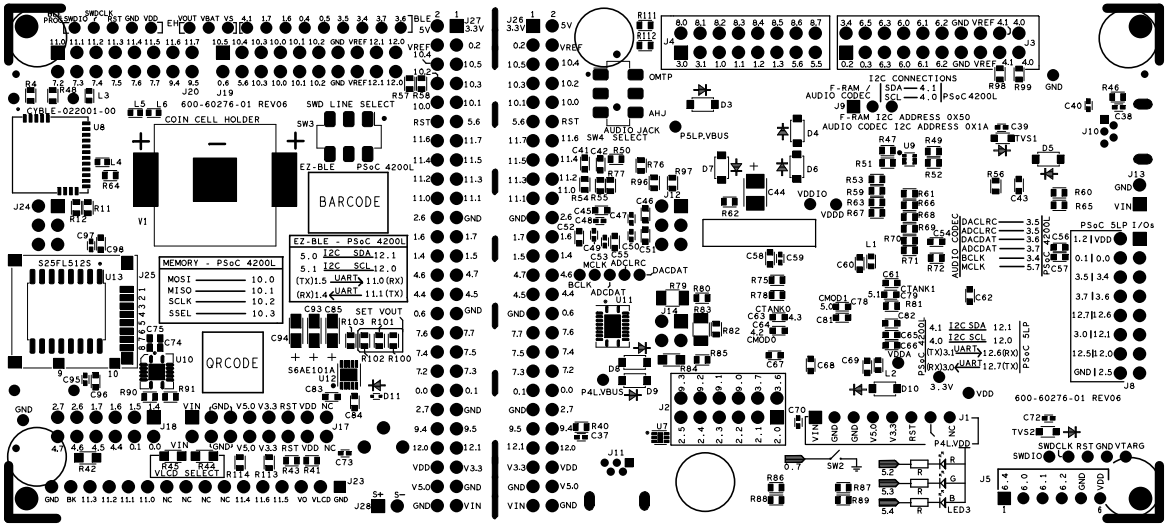
600-60276-01 REV06 POWER LAYER



600-60276-01 REV06 SECONDARY SIDE



600-60276-01 REV06 PRIMARY SILKSCREEN



600-60276-01 REV06 SECONDARY SILKSCREEN